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APPLICATION NO.	F	ILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/779,382	09/779,382 02/08/2001		Adrian P. Wise	94100422(EP)USC1X1C1D11 7169	
22887	7590	07/25/2006		EXAMINER	
DISCOVIS 2265 E. 220		SOCIATES ET		NGUYEN,	DUSTIN
LONG BEA	СН, СА	90810	ART UNIT	PAPER NUMBER	
				2154 DATE MAILED: 07/25/2006	

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)				
		09/779,382	WISE ET AL.				
Office Action Summary		Examiner	Art Unit				
	·	Dustin Nguyen	2154				
	The MAILING DATE of this communication app	• •					
Period fo							
WHIC - Exte after - If NC - Failu Any	ORTENED STATUTORY PERIOD FOR REPLY CHEVER IS LONGER, FROM THE MAILING DANS nsions of time may be available under the provisions of 37 CFR 1.13 SIX (6) MONTHS from the mailing date of this communication. Operiod for reply is specified above, the maximum statutory period were to reply within the set or extended period for reply will, by statute, reply received by the Office later than three months after the mailing ed patent term adjustment. See 37 CFR 1.704(b).	ATE OF THIS COMMUNICATION 36(a). In no event, however, may a reply be tirr vill apply and will expire SIX (6) MONTHS from cause the application to become ABANDONE	N. nely filed the mailing date of this communication. D (35 U.S.C. § 133).				
Status							
1)⊠	Responsive to communication(s) filed on 27 Ag	<u>oril 2006</u> .					
′=	This action is FINAL. 2b) ☐ This action is non-final.						
3)	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is						
closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.							
Disposit	ion of Claims						
5)□ 6)⊠ 7)□	Claim(s) <u>1-5,7-13 and 15-17</u> is/are pending in t 4a) Of the above claim(s) is/are withdray Claim(s) is/are allowed. Claim(s) <u>1-5,7-13 and 15-17</u> is/are rejected. Claim(s) is/are objected to. Claim(s) are subject to restriction and/or	vn from consideration.	·				
Applicat	ion Papers						
10)	The specification is objected to by the Examine The drawing(s) filed on is/are: a) access Applicant may not request that any objection to the Replacement drawing sheet(s) including the correction The oath or declaration is objected to by the Examine.	epted or b) objected to by the liderawing(s) be held in abeyance. See ion is required if the drawing(s) is obj	e 37 CFR 1.85(a). jected to. See 37 CFR 1.121(d).				
Priority (under 35 U.S.C. § 119						
 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No. 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 							
2) Notice	et(s) ce of References Cited (PTO-892) ce of Draftsperson's Patent Drawing Review (PTO-948) mation Disclosure Statement(s) (PTO-1449 or PTO/SB/08)	4) Interview Summary Paper No(s)/Mail Da 5) Notice of Informal P					
	er No(s)/Mail Date <u>05/01/2006</u> .	6) Other:					

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DETAILED ACTION

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1. Claims 1-5, 7-13, 15-17 are presented for examination.

Response to Arguments

2. Applicant's arguments filed 04/27/2006 have been fully considered but they are not persuasive.

- 3. As per remarks, the new amended claims 1 and 10, Applicants' argued that (1) neither Whiting, Dargel, nor Horvath, taken alone or in combination suggest or render obvious a parser, which uses the tokens to prepare the stages for processing.
- 4. As to point (1), Dargel discloses a system includes a pipeline of transformations stages [Figure 1; and Abstract]. The system of Dargel includes a central or master controller [i.e. a parser] [32, Figure 1] communicates with each stage of the pipeline and capable of generating and transmitting transformation control instructions and pixel data from source to the pipeline [i.e. uses the tokens to prepare the stages for processing] [col 2, lines 61-66; and col 6, lines 35-43].

Claim Rejections - 35 USC § 103

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5. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all

obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains.

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Patentability shall not be negatived by the manner in which the invention was made.

6. Claims 1-5 and 10-13 are rejected under 35 U.S.C. 103(a) as being unpatentable

over Whiting et al. [US Patent No 5,016,009], in view of Dargel et al. [US Patent No

4,398,176].

7. As per claim 1, Whiting discloses the invention substantially as claimed including

a decoder interface comprising:

an input circuit that has a port for receiving encoded data from an external source

[232, Figure 7]; and

control circuitry that is coupled to and controls the input circuit [230, Figure 7]

to operate selectively in a first mode [233, Figure 7; and col 18, lines 50-col 19, lines 10

] to receive raw byte data at the port from said external source [i.e. data character stream

[Abstract; col 8, lines 25-28 and col 18, lines 34-36] and a second mode to receive

tokens at the port from said external source [i.e. receive string] [Figure 4; col 8, lines

16-28; col 9, lines 41-48; and col 10, lines 51-56].

Whiting does not specifically disclose a plurality of stages, including an initial

and an intermediate stage, said stages having an associated parser, said tokens having

information used by said parser to prepare said stages for processing.

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Dargel discloses a plurality of stages, including an initial and an intermediate stage [Figure 1], said stages having an associated parser [i.e. central or master controller] [32, Figure 1], said tokens having information used by said parser to prepare said stages for processing [i.e. a central or master controller communicates with each stage of the pipeline and capable of generating and transmitting transformation control instructions and pixel data from source to the pipeline] [col 2, lines 61-66; and col 6, lines 35-43].

It would have been obvious to a person skill in the art at the time the invention was made to combine the teaching of Whiting and Dargel because Dargel's teaching of central or master controller would allow to define all of the particular transformation analysis to be performed in the stages [Dargel, col 2, lines 18-20 and lines 46-49].

- 8. As per claim 2, Whiting discloses wherein the port comprises a coded data port [col 20, lines 42-50].
- 9. As per claim 3, Whiting discloses a microprocessor interface [5, Figure 1a; and col 8, lines 22-25].
- 10. As per claim 4, it is rejected for similar reasons as stated above in claim 3.
- 11. As per claim 5, Whiting discloses the control circuitry includes a byte code signal for selecting the first or second mode [col 18, lines 35-49].

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- 12. As per claims 10-13, they are method claimed of claims 1-5, they are rejected for similar reasons as stated above in claims 1-5.
- 13. Claims 7-9, and 15-17 are rejected under 35 U.S.C. 103(a) as being unpatentable over Whiting et al. [US Patent No 5,016,009], in view of Dargel et al. [US Patent No 4,398,176], and further in view of Horvath et al. [US Patent No 5,450,599].
- 14. As per claim 7, Whiting and Dargel do not specifically disclose the received raw byte data is placed into tokens. Horvath discloses the received raw byte data is placed into tokens [col 8, lines 3-24]. It would have been obvious to a person skill in the art at the time the invention was made to combine the teaching of Whiting, Dargel and Horvath because Horvath's teaching would allow to reduce processing overhead to increase system performance.
- 15. As per claim 8, Horvath discloses the first byte of the raw byte data causes a token header to be generated [Figure 2; and col 4, lines 15-30].
- 16. As per claim 9, Horvath discloses subsequent bytes of the raw byte data appended to the token header to form tokens [Figure 2; and col 4, lines 42-58].
- 17. As per claims 15-17, they are method claimed of claims 7-9, they are rejected for similar reasons as stated above in claims 7-9.

18. THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Dustin Nguyen whose telephone number is (571) 272-3971. The examiner can normally be reached on flex schedule.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Follansbee John can be reached on (571) 272-3964. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Dustin Nguyen Examiner Art Unit 2154

JOHN FOLLANSBEE SUPERVISORY PATENT EXAMINER TEOMOLOGY CENTER 2100